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(Not for submission under 37 CFR 1.99)

Application Number	10550323
Filing Date	2005-09-23
First Named Inventor	Roger D. Chamberlain
Art Unit	2162
Examiner Name	Fleurantin, Jean B.
Attorney Docket Number	53047-57370

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	2	6064739	A	2000-05-16	Davis	
	3	6279140		2001-08-21	Slane	
	4	7257842		2007-08-14	Barton et al.	
	5	7454418		2008-11-18	Wang	
	6	7478431		2009-01-13	Nachenberg	
	7	7496108		2009-02-24	Biran et al.	
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	9	7617291		2009-11-10	Fan et al.	
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	1	2000286715	JP	A	2000-10-13	Toshiba Corp		☐
	2	2002101089	JP	A	2002-04-05	Mitsubishi Corp		☐
	3	2000041136	WO	A1	2000-07-13	Silicon Graphics Inc		☐

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	1	AMANUMA ET AL., "A FPGA Architecture For High Speed Computation", Proceedings of 60th Convention Architecture, Software Science, Engineering, March 14, 2000, pp. 1-163-1-164, Information Processing Society, Japan	☐
	2	ASAMI ET AL., "Improvement of DES Key Search on FPGA-Based Parallel Machine "RASH"", Proceedings of Information Processing Society, August 15, 2000, pp. 50-57, Vol. 41, No. SIG5 (HPS1), Japan	☐
	3	DHARMAPURIKAR ET AL., "Design and Implementation of a String Matching System for Network Intrusion Detection using FPGA-based Bloom Filters", Proc. of 12th Annual IEEE Symposium on Field Programmable Custom Computing Machines, 2004, pp. 1-10	☐
	4	KOLONIARI ET AL., "Content-Based Routing of Path Queries in Peer-to-Peer Systems", pp. 1-19, E. Bertino et al. (Eds.): EDBT 2004, LNCS 2992, pp. 29-47, 2004, copyright by Springer-Verlag, Germany	☐
	5	LI ET AL., "Large-Scale IP Traceback in High-Speed Internet: Practical Techniques and Theoretical Foundation", Proceedings of the 2004 IEEE Symposium on Security and Privacy, 2004, pp. 1-15	☐
	6	Office Action for JP Application 2006-533393 dated August 24, 2010	☐

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	7	SEKI ET AL., "High Speed Computation of Shogi With FPGA", Proceedings of 58th Convention Architecture, Software Science, Engineering, March 9, 1999, pp. 1-133-1-134	☐
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